On the merits, the Office Action rejected Claim 6 under 35 U.S.C. § 102(b) as being anticipated by Meyer et al. (WO 8503807; hereinafter "Meyer"). The Office Action also rejected Claim 8 under 35 U.S.C. § 103(a) as being unpatentable over Meyer in view of Wondrak et al. (U.S. Patent No. 5,578,859; hereinafter "Wondrak"). Applicants submit that the pending claims are allowable over the cited art for at least the following reasons.

Applicants' Claim 6 recites: "[a] semiconductor device comprising a semiconductor body having a first region of a first conductivity type and, adjacent thereto, a second region of the second, opposite, conductivity type, wherein the first region electrically insulates the second region from a substrate of the second conductivity type, a third region of the first conductivity type, which is adjacent the second region and separated from the first region by the second region, and a fourth region of the first conductivity type which is separated from the second region by the third region and which has a higher doping concentration than the third region, the first, the second and the fourth region being provided with a terminal, characterized in that the third region is provided with a protection zone of the first conductivity type having a higher doping concentration than the third region, which protection zone is separated from the second region by the third region and is situated near the fourth region, and separated from

said fourth region by an intermediate, comparatively high-impedance region, characterized in that the third region and the fourth region form, respectively, a drift region and a drain region of a Lateral DMOS transistor."

Meyer fails to recite or suggest the first region electrically insulating the second region from a substrate of the second conductivity type. Consequently, with regard to voltage in the reverse direction, Meyer's device will operate in an entirely different way in protecting the forth region from breakdown. Consequently, Claim 6 is believed patentable over Meyer for at least these reasons.

Claim 8 recites a semiconductor device substantially corresponding to Claim 6 and is believed patentable for at least the same reasons. In addition, withdrawal of the § 103 rejection of Claim 8 is respectfully requested, as it is now believed moot in light of the above amendments and remarks.

In view of the foregoing, it is respectfully submitted that the currently-pending claims clearly define statutory subject matter. Accordingly, allowance of the currently-pending claims is now respectfully submitted to be justified, and favorable consideration is earnestly solicited.

Respectfully submitted,

By\_

Aaron Waxler, Reg. No. 48,027

(914) 333-9608

August 20, 2002

## APPENDIX A

## MARKED-UP CLAIMS

(Thrice Amended) A semiconductor device comprising a semiconductor body having a first region of a first conductivity type and, adjacent thereto, a second region of the second, opposite, conductivity type, wherein the first region electrically insulates the second region from a substrate of the second conductivity type, a third region of the first conductivity type, which is adjacent the second region and separated from the first region by the second region, and a fourth region of the first conductivity type which is separated from the second region by the third region and which has a higher doping concentration than the third region, the first, the second and the fourth region being provided with a terminal, characterized in that the third region is provided with a protection zone of the first conductivity type having a higher doping concentration than the third region, which protection zone is separated from the second region by the third region and is situated near the fourth region, and separated from said fourth region by an intermediate, comparatively high-impedance region, characterized in that the third region and the fourth

From-PHILIPS ELECTRON

region form, respectively, a drift region and a drain region of a Lateral DMOS transistor.

(Thrice Amended) A semiconductor device comprising a semiconductor body having a first region of a first conductivity type and, adjacent thereto, a second region of the second, opposite, conductivity type, wherein the first region electrically insulates the second region from a substrate of the second conductivity type, a third region of the first conductivity type, which is adjacent the second region and separated from the first region by the second region, and a fourth region of the first conductivity type which is separated from the second region by the third region and which has a higher doping concentration than the third region, the first, the second and the fourth region being provided with a terminal, characterized in that the third region is provided with a protection zone of the first conductivity type having a higher doping concentration than the third region, which protection zone is separated from the second region by the third region and is situated near the fourth region, and separated from said fourth region by an intermediate, comparatively high-impedance region, characterized in that the device is of the RESURF type, wherein the product of the thickness and the doping concentration of the third region is approximately 1012 atoms per cm2.